

$V_{DSS}, 30V$ $R_{DS(ON)}, 11.5m\Omega (max.) @ V_{GS}=10V$ $R_{DS(ON)}, 17m\Omega (max.) @ V_{GS}=4.5V$ $I_D, 40A$	<b>T0-251</b>	

Description	Features
The SG30N05I uses advanced Trench technology and designs to provide excellent $R_{DS(ON)}$ with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.	<ul style="list-style-type: none"> <li>• Low On-Resistance</li> <li>• Low Input Capacitance</li> <li>• Low Miller Charge</li> <li>• Low Input/Output Leakage</li> </ul>
	Applications
	<ul style="list-style-type: none"> <li>• Motor / Body Load Control</li> <li>• Automotive Systems</li> <li>• Load Switch</li> <li>• DC-DC converters and Off-line UPS</li> </ul>

**Ordering Information**

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SG30N05I	Halogen-Free	T0-251	I	Box (Tube)	6,750 (75)

**Absolute Maximum Ratings ( $T_A=25^\circ C$  unless otherwise noted)**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	$T_C=25^\circ C$	40
		$T_C=70^\circ C$	33.6
Drain Current-Pulsed <sup>Note 1</sup>	$I_{DM}$	120	A
Drain Current-Continuous	$I_D$	$T_A=25^\circ C$	10.7
		$T_A=70^\circ C$	8.6
Avalanche Current, $L=0.1mH$	$I_{AS}$	15	A
Avalanche Energy, $L=0.1mH$	$E_{AS}$	11.25	mJ
Maximum Power Dissipation	$P_D$	$T_C=25^\circ C$	30.4
		$T_C=70^\circ C$	19.5
		$T_A=25^\circ C$	2
		$T_A=70^\circ C$	1.3
Storage Temperature Range	$T_{STG}$	-55 to +150	$^\circ C$
Operating Junction Temperature Range	$T_J$	-55 to +150	$^\circ C$

**Thermal Resistance Ratings**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Maximum Junction-to-Ambient <sup>Note 2</sup>	$R_{\theta JA}$	Steady State	-	-	62	$^\circ C/W$
Maximum Junction-to-Case	$R_{\theta JC}$	Steady State	-	-	4.1	$^\circ C/W$

**Electrical Characteristics ( $T_j=25^{\circ}\text{C}$  unless otherwise noted)**

<b>OFF CHARACTERISTICS</b>						
<i>Parameter</i>	<i>Symbol</i>	<i>Conditions</i>	<i>Min.</i>	<i>Typ.</i>	<i>Max.</i>	<i>Unit</i>
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS}=0\text{V}, I_{DS}=250\mu\text{A}$	30	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=24\text{V}, V_{GS}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-Body Leakage	$I_{GSS}$	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	-	-	$\pm 100$	nA

<b>ON CHARACTERISTICS</b>						
<i>Parameter</i>	<i>Symbol</i>	<i>Conditions</i>	<i>Min.</i>	<i>Typ.</i>	<i>Max.</i>	<i>Unit</i>
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_{DS}=250\mu\text{A}$	1.2	-	2.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}, I_{DS}=10\text{A}$	-	9	11.5	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_{DS}=8\text{A}$	-	13	17	$\text{m}\Omega$

<b>DYNAMIC CHARACTERISTICS</b>						
<i>Parameter</i>	<i>Symbol</i>	<i>Conditions</i>	<i>Min.</i>	<i>Typ.</i>	<i>Max.</i>	<i>Unit</i>
Input Capacitance	$C_{iss}$	$V_{DS}=15\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$	-	580	-	pF
Output Capacitance	$C_{oss}$		-	95	-	
Reverse Transfer Capacitance	$C_{rss}$		-	60	-	

<b>SWITCHING CHARACTERISTICS</b>						
<i>Parameter</i>	<i>Symbol</i>	<i>Conditions</i>	<i>Min.</i>	<i>Typ.</i>	<i>Max.</i>	<i>Unit</i>
Turn-On Delay Time	$T_{d(on)}$	$V_{DD}=15\text{V}, I_D=10\text{A}, V_{GEN}=4.5\text{V}, R_g=3\Omega$	-	15	-	ns
Rise Time	$t_r$		-	12	-	
Turn-Off Delay Time	$T_{d(off)}$		-	14	-	
Fall Time	$t_f$		-	10	-	
Total Gate Charge at 10V	$Q_g$	$V_{DS}=15\text{V}, I_{DS}=10\text{A}, V_{GS}=4.5\text{V}$	-	8	-	nC
Gate to Source Gate Charge	$Q_{gs}$		-	3	-	
Gate to Drain "Miller" Charge	$Q_{gd}$		-	4.5	-	

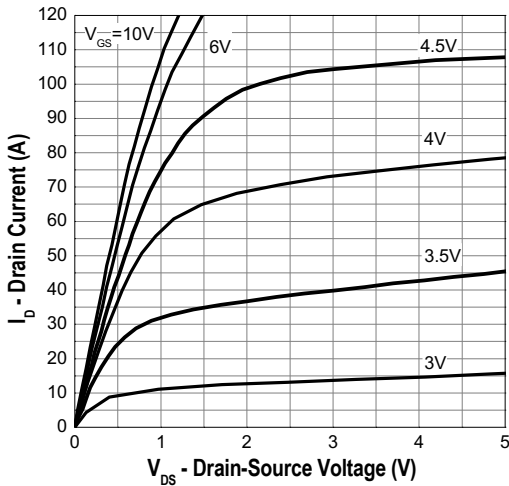
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
<i>Parameter</i>	<i>Symbol</i>	<i>Conditions</i>	<i>Min.</i>	<i>Typ.</i>	<i>Max.</i>	<i>Unit</i>
Drain-Source Diode Forward Voltage	$V_{SD}$	$V_{GS}=0\text{V}, I_S=10\text{A}$	-	-	1.3	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F=10\text{A}, dI/dt=100\text{A}/\mu\text{s}$	-	14	-	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$		-	11	-	nC

**Notes:**

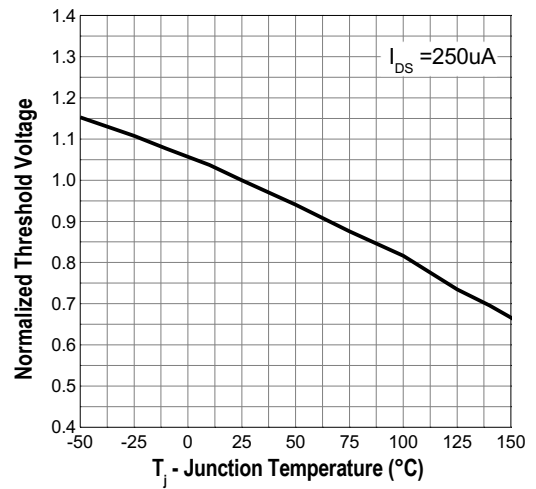
- Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
- $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\theta JC}$  is guaranteed by design while  $R_{\theta CA}$  is determined by the user's board design.  $R_{\theta JA}$  shown below for single device operation on FR-4 in still air.

**Typical Operating Characteristics**

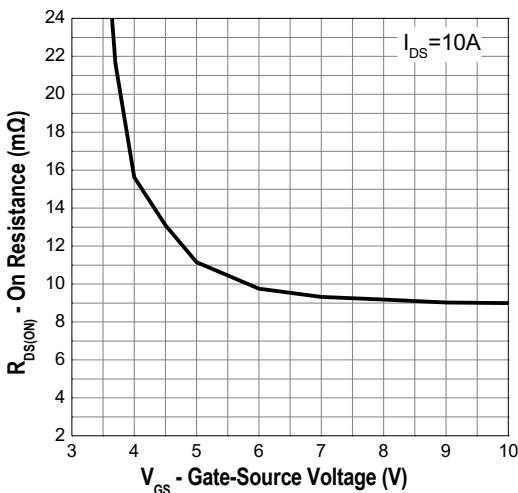
**Output Characteristics**



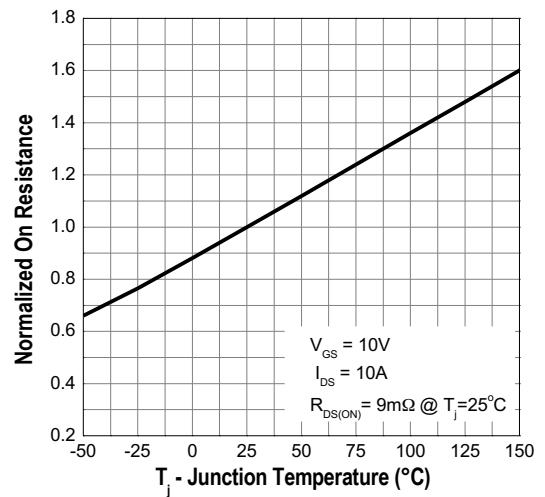
**Gate Threshold Voltage**



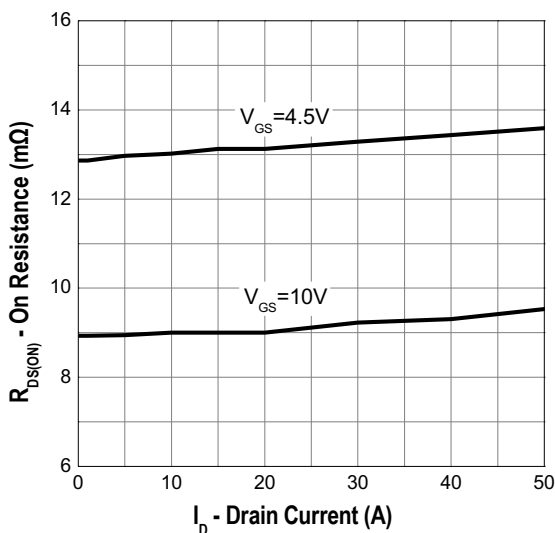
**Gate-Source On Resistance**



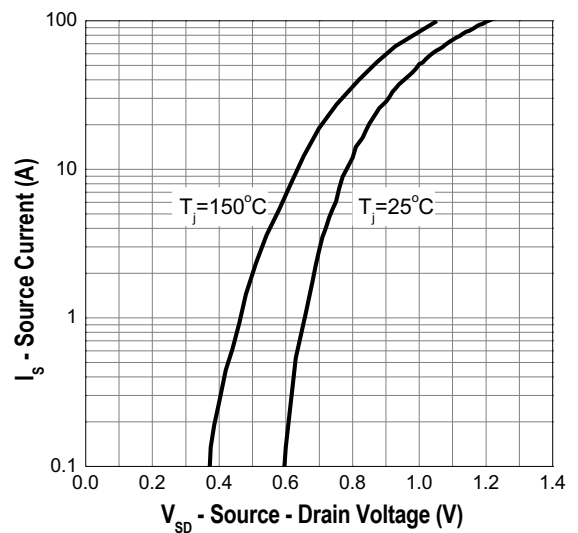
**Drain-Source On Resistance**



**Drain-Source On Resistance**

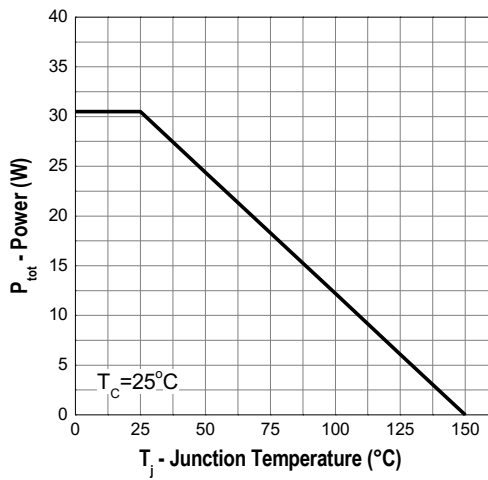


**Source-Drain Diode Forward**

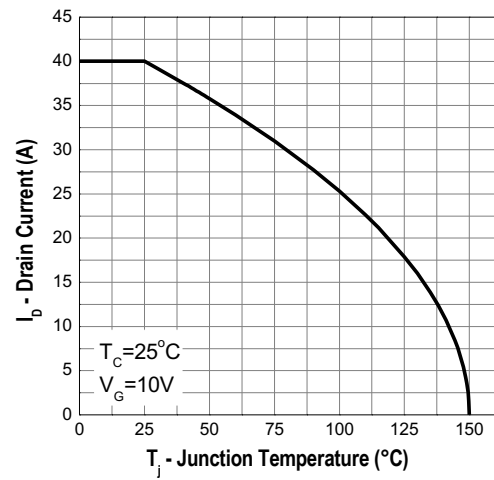


## Typical Operating Characteristics (Cont.)

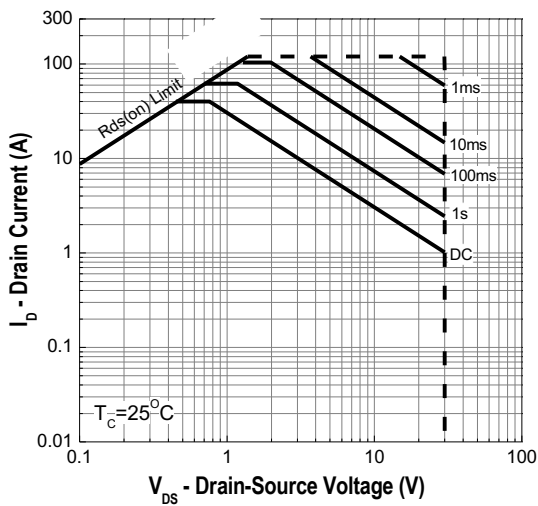
**Power Dissipation**



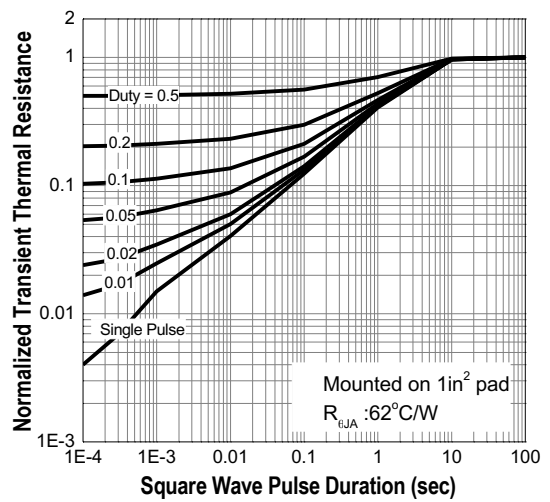
**Drain Current**



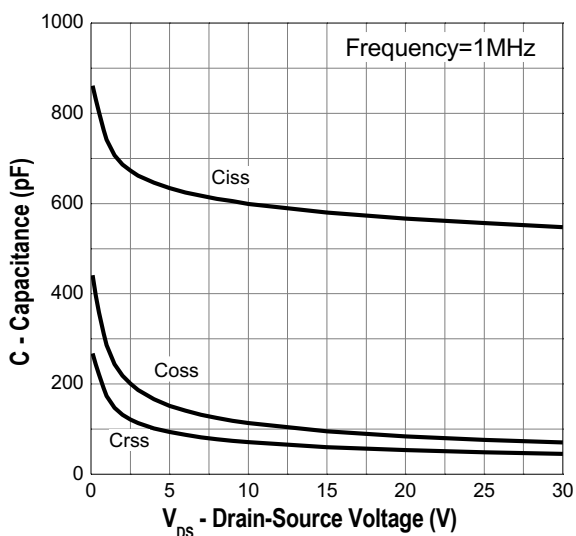
**Safe Operation Area**



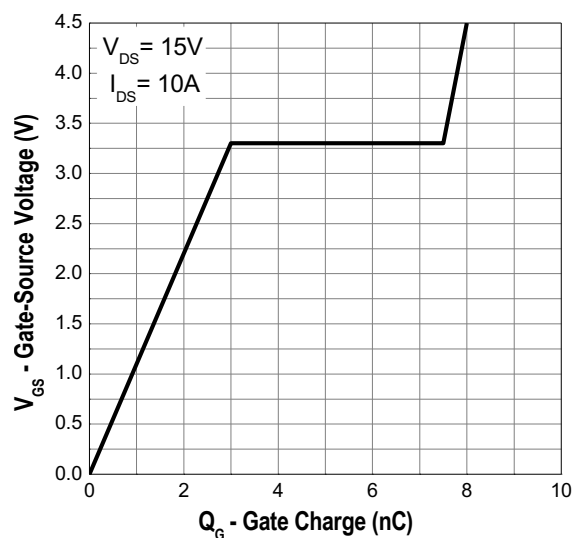
**Transient Thermal Impedance**



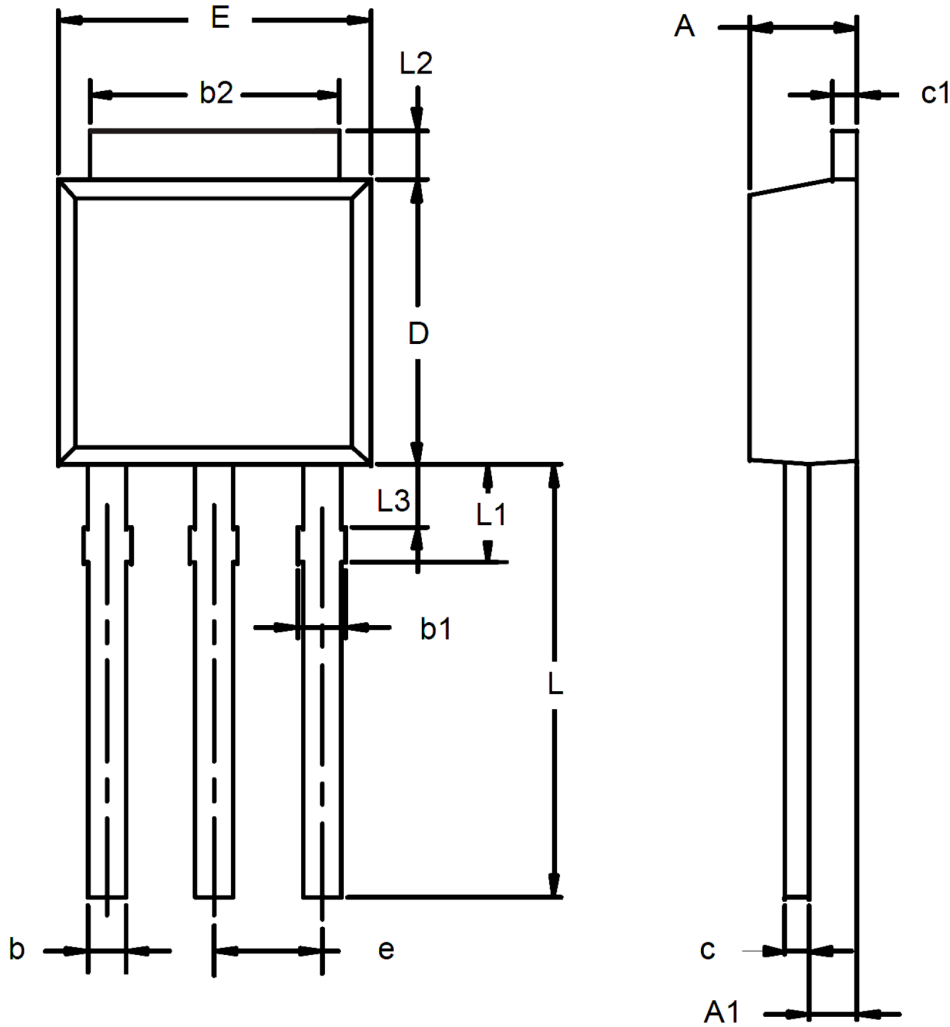
**Capacitance**



**Gate Charge**



**Package Dimension**



**Dimensions**

Symbols	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.21	-	2.38	0.087	-	0.094
A1	0.89	-	1.14	0.035	-	0.045
b	0.71	-	0.89	0.028	-	0.035
b1	0.76	-	1.14	0.030	-	0.045
b2	5.23	-	5.43	0.206	-	0.214
c	0.46	-	0.58	0.018	-	0.023
c1	0.46	-	0.58	0.018	-	0.023
D	5.97	-	6.22	0.235	-	0.245
E	6.48	-	6.73	0.255	-	0.265
e	2.28 BSC			0.090 BSC		
L	8.89	-	9.53	0.350	-	0.375
L1	1.91	-	2.28	0.075	-	0.090
L2	0.89	-	1.27	0.035	-	0.050
L3	1.15	-	1.52	0.045	-	0.060

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